

Download Altera Up Simulator

Note: ModelSim*-Intel® FPGA edition software supports designs of up to 3,000 instances. Instances from our pre-compiled libraries do not count towards the 3,000 instance limitation. Instances from our pre-compiled libraries do not count towards the 3,000 instance limitation. Depending on your install, your simulator resolution may be picosecond by default. Check your modelsim.ini and look for the Resolution variable under the [vsim] header. Alternatively you can force the resolution on the command line. You are very close with your example. The syntax is vsim -t ns for nanosecond resolution. software, and automates the compilation and simulation of testbenches. Setting Up the EDA Simulator Execution Path To run an EDA simulator (e.g. Modelsim-Altera) automatically from the Quartus II software using the NativeLink feature, specify the path to your simulation tool by performing the following steps: 1. On the Tools menu, click Options. The simulator can be started from within the Waveform Editor, or by using the Altera NativeLink flow. For Quartus software v10.1 through 12.1, the Waveform Editor tool could be used only to enter test inputs and set output signals to view.